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XPP-VC: AC Compiler with Temporal Partitioning for the PACT-XPP Architecture

JMP Cardoso, M Weinhardt - LECTURE NOTES IN COMPUTER SCIENCE, 2002 - Springer

... ALU array. ... To decide on a certain merge the compiler considers that a partition must define, on the control flow graph of the program, a region of code ...

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Reconfigurable computing: what, why, and implications for design automation -

► [cinvestav.mx](#) (PDF)

A DeHon, J Wawrzynek - Proceedings of the 36th ACM/IEEE conference on Design ..., 1999 - portal.acm.org

... processors, this shows up as the ALU datapath width ... can use techniques like hardware-software partitioning to utilize the available array capacity to ...

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[PDF] ► A partitioning compiler for computers with Xputer-based accelerators

J Becker - 1997 - xputers.informatik.uni-kl.de

... the possibility of guiding the first level partitioning process ... user acceptance and the compatibility of commercially available software. ... KRAA : Kress Alu array ...

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Parallelization in co-compilation for configurable accelerators-ahost/accelerator partitioning ... -

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J Becker, R Hartenstein, M Herz, U Nageldinger - Design Automation Conference 1998. Proceedings of the ASP- ..., 1998 - ieeexplore.ieee.org

... vironment, where automation of hostkcelerator partition- ing and ... also be used for hardware/software co-design ... coupling between instruction sequencer and ALU [31 ...

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Hardware/software co-design - ► [lamu.edu](#) (PDF)

G De Michell, RK Gupta - Proceedings of the IEEE, 1997 - ieeexplore.ieee.org

... transistors driven by memory arrays [110] or ... objectives may require a specific hardware/software partition. ... can reduce the software execution time, when they ...

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Toward a dataflow/von Neumann hybrid architecture - ► [pdx.edu](#) (PDF)

RA Iannucci - ACM SIGARCH Computer Architecture News, 1988 - portal.acm.org

... Multiphase processing will always minimize latency cost over ... cannot be made independent of program inputs or ... he viewed as a constraint on partitioning since two ...

Cited by 157 - Related articles - Web Search - All 4 versions

Thread partitioning and scheduling based on cost model

X Tang, J Wang, KB Theobald, GR Gao - Proceedings of the ninth annual ACM symposium on Parallel ..., 1997 - portal.acm.org

... the other hand, since Sz is an ALU operation, it ... 3. Total execution time of the program is minimized ... Producing a correct partition guarantees that the pro- gram ...

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Automatic compilation to a coarse-grained reconfigurable system-opn-chip - ► [ucr.edu](#) (PDF)

G Venkataramani, W Najjar, F Kurdahi, N ... - ACM Transactions on Embedded Computing Systems (TECS), 2003 - portal.acm.org
 ... control the input multiplexers, the ALU/multiplier, and ... The proposed compiler is meant to partition program execution ... be created from a linear array of ALUs ...
 Cited by 28 - Related articles - Web Search - All 6 versions

Variable partitioning for dual memory bank DSPs - ►uni-dortmund.de (pdf)
 R Leupers, D Kotte - Acoustics, Speech, and Signal Processing, 2001. Proceedings. ..., 2001 - ieeexplore.ieee.org
 ... for a hardware multiplier, an ALU, as well ... For very large programs, the ILP partitioning technique ... presented a new technique for variable partitioning in C ...
 Cited by 39 - Related articles - Web Search - BL Direct - All 12 versions

A Dynamic Multithreading Processor - ►princeton.edu (pdf)
 H Akkary, MA Driscoll - MICRO-ANNUAL WORKSHOP THEN ANNUAL INTERNATIONAL SYMPOSIUM-, 1998 - doi.ieeecs.org
 ... presented in [7]. SM uses hardware to partition a program ... Stores are issued to memory in program order and compete ... The latencies are 1 cycle for the ALU, 3 for ...
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